

<b>Notice of References Cited</b>		Application/Control No. 09/692,297	Applicant(s)/Patent Under Reexamination BALLAM, PETER	
		Examiner Crystal J. Barnes	Art Unit 2121	Page 1 of 1

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K	US-			
L	US-			
M	US-			

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**NON-PATENT DOCUMENTS**

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	V	Troy Scott, "Adopting VHDL for PLD design and simulation", EDN, 9 April 1998, Volume 43, Issue 8, Pages 139-145.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.